UNISONIC TECHNOLOGIES CO., LTD

TDA2050

LINEAR INTEGRATED CIRCUIT

32W HI-FI AUDIO POWER **AMPLIFIER**

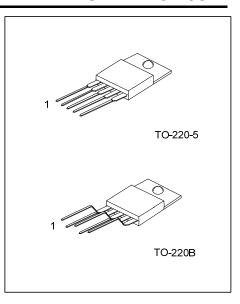
DESCRIPTION

The UTC TDA2050 is a monolithic integrated circuit with high power capability and is designed to use as an class AB audio amplifier. It can deliver typically 50W music power into 4Ω load over 1 sec at V_S =22.5V, f = 1KHz.

The device is most suitable for both Hi-Fi and high class TV sets on the strength of its high supply voltage and very low harmonic and crossover distortion.

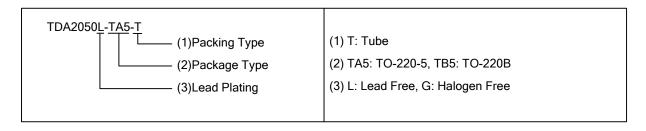
FEATURES

- * High output power (50W Music Power IEC 268.3 Rules)
- * High operating supply voltage (50V)
- * Single or split supply operations
- * Very low distortion
- * Short circuit protection (OUT to GND)
- * Thermal shutdown

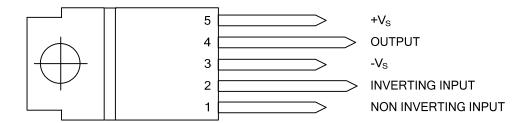


ORDERING INFORMATION

Orderin	Package	Dooking	
Lead Free	ad Free Halogen Free		Packing
TDA2050L-TA5-T	TDA2050L-TA5-T TDA2050G-TA5-T		Tube
TDA2050L-TB5-T	TDA2050G-TB5-T	TO-220B	Tube

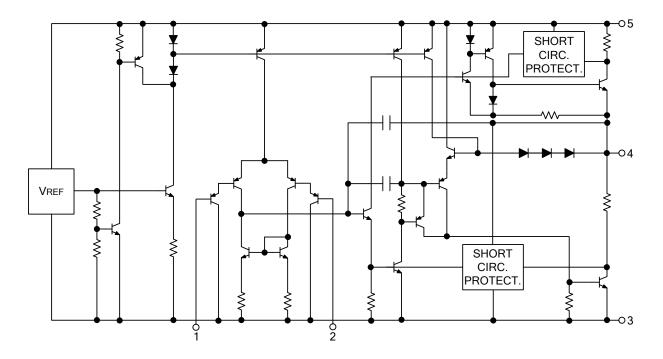


■ PIN CONFIGURATION



*TAB CONNECTED TO PIN 3

■ BLOCK DIAGRAM





■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	Vs	±25	V
Input Voltage	V_{IN}	V_{S}	V
Differential Input Voltage	V _{IN(DIFF)}	±15	V
Output Peak Current (internally limited)	I _{OUT}	5	Α
Power Dissipation T _C = 75°C	P _D	25	W
Junction Temperature	TJ	+150	°C
Storage Temperature	T _{STG}	-40 ~ +150	°C

Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Thermal Resistance junction-case	θ_{JC}	3	°C/W

■ ELECTRICAL CHARACTERISTICS

(Refer to the Test Circuit, $V_S = \pm 18V$, $T_A = 25$ °C, f = 1 kHz, unless otherwise specified.)

PARAMETE	ĒR	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Supply Voltage		Vs			±4.5		±25	V
Quiescent Drain Current			V _S =±4.5V			18	50	
		I _D	V _S =±25V			21	90	mA
Input Bias Current		I _B	V _S =±22V			0.4	0.5	μΑ
Input Offset Voltage		V _{IN(OS)}	V _S =±22V				±15	mV
Input Offset Current		I _{IN(OS)}	V _S =±22V				±200	nA
			$R_L=4\Omega$		24	27		
	D = 0.5%		$R_L=8\Omega$			18		
PMS Output Dower			$R_L=8\Omega, V_S$	_S =±22V	22	25		
RMS Output Power		Po	$R_L=4\Omega$			35		W
	D = 10%		$R_L=8\Omega$			22		
			R _L =8Ω, V ₃	_S =±22V		32		
Music Power IEC268.	3 RULES		D=10%, T=1s, V _S =±22.5V, R _L =4Ω			50		
			$R_L=4\Omega$	f=1kHz, P ₀ =0.1~24W		0.03	0.5	
				f =100Hz~10kHz,			0.5	
Total Harmonic Distor	tion	THD		P _O =0.1~18W			0.5	%
Total Harmonic Distor	tion		$R_L=8\Omega$, $V_S=\pm22V$	f=1kHz, P ₀ =0.1~20W		0.02		
				f=100Hz~10kHz,			0.5	
				P _O =0.1~15W			0.5	
Slew Rate		SR			5	8		V/µs
Open Loop Voltage G	ain	G_V				80		dB
Closed Loop Voltage Gain G _V				30	30.5	31	dB	
Power Bandwidth (-3dB) B_W $R_L=4\Omega$, $V_{IN}=200$		_N =200mV	20 ~ 80000		Hz			
Total Input Noise		e _N	Curve A			4		μV
			B=22Hz~22kHz			5	10	μν
Input Resistance (pin	istance (pin 1) R _{IN}		500			kΩ		
Supply Voltage Rejection		SVR	R_S =22K Ω	, f=100Hz, V _{RIPPLE} =0.5Vrms		45		dB
Efficiency		l n	$P_O=28W$, $R_L=4\Omega$			65		%
			P_0 =25W, R_L =8 Ω , V_S =±22V			67		70



■ TYPICAL APPLICATION CIRCUIT

FOR SPLIT SUPPLY APPLICATION SUGGESTIONS

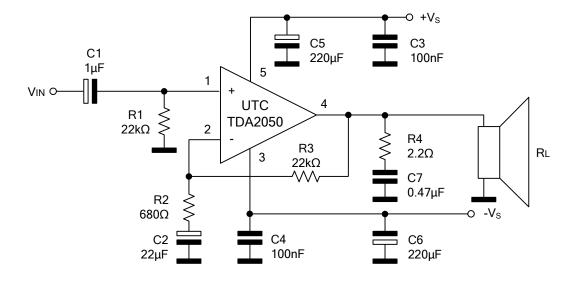


Figure.1 Split Supply Typical Application Circuit

The following table demonstrates the recommended values of the external components are those shown on above circuit. Different values can be used.

COMPONENT	DUDDOOF	RECOMMENDED VALUE			
COMPONENT	PURPOSE	TYPICAL	LARGER	SMALLER	
R1	Input Impedance	22kΩ	Increase of Input Impedance	Decrease of Input Impedance	
R2	Feedback Resistor	680Ω	Decrease of Gain*	Increase of Gain	
R3		22kΩ	Increase of Gain	Decrease of Gain*	
R4	Frequency Stability	2.2Ω	Danger of Oscillations		
C1	Input Decoupling DC	1µF		Higher Low-frequency cut-off	
C2	Inverting Input DC Decoupling	22µF	Increase of Switch ON/OFF Noise	Higher Low-frequency cut-off	
C3, C4	Supply Voltage Bypass	100nF		Danger of Oscillations	
C5, C6	Supply Voltage Bypass	220µF		Danger of Oscillations	
C7	Frequency Stability	0.47µF		Danger of Oscillations	

^{*} The gain must be higher than 24dB



■ TYPICAL APPLICATION CIRCUIT(CONT.)

FOR SINGLE SUPPLY APPLICATION SUGGESTIONS

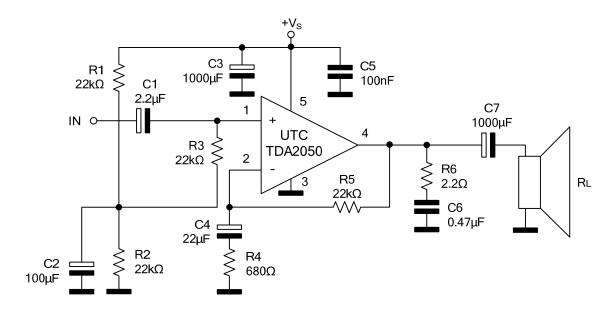


Figure.2 Single Supply Typical Application Circuit

The following table demonstrates the recommended values of the external components are those shown on above circuit. Different values can be used.

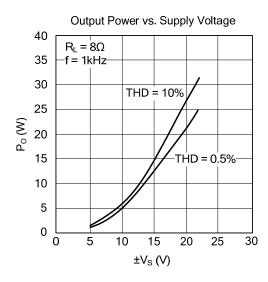
		RECOMMENDED VALUE			
COMPONENT	PURPOSE	T) (DIOAI	1		
		TYPICAL	LARGER	SMALLER	
R1, R2, R3	Biasing Resistor	22kΩ			
R4	Feedback Resistor	680Ω	Decrease of Gain*	Increase of Gain	
R5	reeuback Resistor	22kΩ	Increase of Gain	Decrease of Gain*	
R6	Frequency Stability	2.2Ω	Danger of Oscillations		
C1	Non-Inverting Input Decoupling DC	2.2µF		Higher Low-frequency cut-off	
C2	Supply Voltage Rejection	100µF	Worse Turn-off Transient Worse Turn-on Delay		
C3	Supply Voltage Bypass	1000µF		Danger of Oscillations Worse of Turn-off Transient	
C4	Inverting Input DC Decoupling	22µF	Increase of Switch ON/OFF	Higher Low-frequency cut-off	
C5	Supply Voltage Bypass	100nF		Danger of Oscillations	
C6	Frequency Stability	0.47µF		Danger of Oscillations	
C7	Output DC Decoupling	1000µF		Higher Low-frequency cut-off	

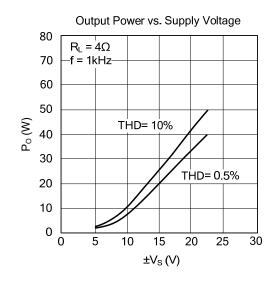
^{*} The gain must be higher than 24dB

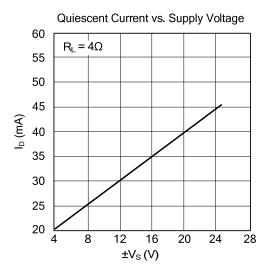
Note: If the supply voltage is lower than 40V and the load is 8Ω (or more), a lower value of C2(i.e. $22\mu F$) can be used. C7 can be larger than $1000\mu F$ only if the supply voltage does not exceed 40V.

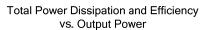


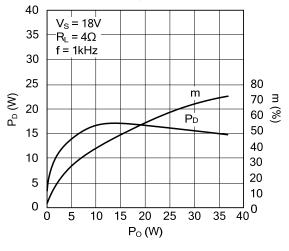
■ TYPICAL CHARACTERISTICS (Split Supply Test Circuit, unless otherwise specified)



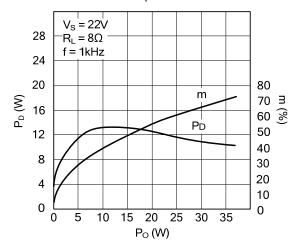








Total Power Dissipation and Efficiency vs. Output Power





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